



00862.022497.1

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
:)
KIYOFUMI SAKAGUCHI, et al.)
:)
Application No.: 10/687,743)
:)
Filed: October 20, 2003)
:)
For: THIN-FILM SEMICONDUCTOR)
:)
DEVICE AND METHOD OF)
:)
MANUFACTURING THE SAME) November 19, 2003

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUBMISSION OF CORRECTED FORM PTO-1449

Sir:

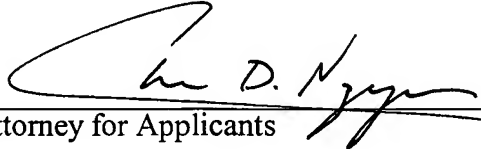
In the Information Disclosure Statement dated October 17, 2003, U.S.

Patent No. 6,136,688 was listed in error.

Accordingly, Applicants hereby submit a corrected Form PTO-1449 for consideration by the Examiner, in which the aforementioned U.S. Patent No. 6,136,688 is omitted.

Applicants' undersigned attorney may be reached in our Costa Mesa, California office by telephone at (714) 540-8700. All correspondence should continue to be directed to our address given below.


Respectfully submitted,


Attorney for Applicants

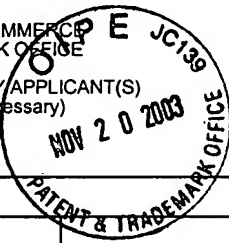
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FORM PTO 1449 (modified) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE LIST OF REFERENCES CITED BY APPLICANT(S) (Use several sheets if necessary)				ATTY DOCKET NO. 00862.022497.1		APPLICATION NO. 10/687,743	
				APPLICANT KIYOFUMI SAKAGUCHI, et al.			
				FILING DATE October 20, 2003		GROUP NYA	
U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
	5,206,749	04/27/93	Zavracky et al.	359	59		
	5,256,562	10/26/93	Vu et al.	437	86		
	5,811,348	09/22/98	Matsushita et al.	438	455		
	6,107,213	08/22/00	Tayanaka et al.	438	762		
	5,856,229	01/05/99	Sakaguchi et al.	438	406		
	5,985,742	11/16/99	Henley et al.	438	515		
	6,075,280	06/13/20	Yung et al.	257	620		
	6,136,668	10/24/00	Tamaki et al.	438	462		
	6,186,384	02/13/01	Sawada	225	2		
	6,190,937	02/20/01	Nauagawa, et al.	438	67		
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	6,258,698	07/10/01	Iwasaki, et al.	438	455		
	6,306,729	10/23/01	Sauaguchi, et al.	438	455		
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	6,342,433	01/29/02	Ohmi, et al.	438	455		
	6,382,292	05/07/02	Ohmi, et al.	156	584		
	6,465,329	10/15/02	Glenn	438	642		
	2002/0076904	06/20/02	Imler	438	642		
	2002/0100941	08/01/02	Yonehara et al.	257	359		
EXAMINER				DATE CONSIDERED			

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		2002/0102758	08/01/02	Yonehara et al.	438	30	
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO/ OR ABSTRACT
		1 122 794	08/08/01	Europe			
		858 110	08/12/98	Europe			
		849 788	06/24/98	Europe			
		886 300	12/23/98	Europe			
		9-312349	12/02/97	Japan			
		11-316397	11/16/99	Japan			
OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)							
		Shimoda, T., et al: "Surface Free Technology By Laser Annealing (SUFTLA)" International Electron Devices Meeting 1999. IEDM. Technical Digest. Washington, DC, Dec. 5-8, 1999, New York, NY: IEEE, US, Aug. 1, 1999 (1999-08-01), pages 289-292, XP000933199 ISBN: 0-7803-5411-7.					
EXAMINER				DATE CONSIDERED			

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